PSpice Designer & PSpice Designer Plus
Features comparison

Feature	OrCAD PSpice Designer	OrCAD PSpice Designer Plus
• SCHEMATIC ENTRY + DATA MANAGEMENT		
Graphical, flat and hierarchical page editor and Picture block hierarchy	•	•
Unlimited undo/redo	•	•
Net Groups - Complex bus definition	•	•
Intelligent PDF creation	•	•
AutoWire	•	•
Design reuse	•	•
3D Footprint Viewer	•	•
Coloured Components / nets	•	•
Tcl scripting customization	•	•
Online design rule check including custom DRC capability and Waive DRC	•	•
PCB Forward and back-annotation of properties / pin-and-gate swaps	•	•
Schematic Part and Library editor	•	•
Cross-probing and cross-placement	•	•
FPGA design-in / pin import & export and FPGA bi-directional support	•	•
Multiple PCB netlist interfaces	•	•
SI Topology creation	•	•
Digi-Key (PartLink App) Component Parametric data directly from web	•	•
Property editor for pins, components, nets	•	•
Design differences viewer	•	•

• Fonction incluse

Feature	OrCAD PSpice Designer	OrCAD PSpice Designer Plus
Intelligent PDF creation	•	•
Component Information System		
Centralized part information system	CIS option	CIS option
Relational data support	CIS option	CIS option
ODBC-compliant database support	CIS option	CIS option
Graphical preview of database parts	CIS option	CIS option
Intelligent database query	CIS option	CIS option
Component property validation	CIS option	CIS option
Interface to relational database and management systems	CIS option	CIS option
Database query for part selection and parametric properties	CIS option	CIS option
Extensive reports and report templates	CIS option	CIS option
Crystal Reports™ for advanced documentation	CIS option	CIS option
Unlimited assembly variant support	CIS option	CIS option
Schematic and BOM Variants Manager (Parts not Fitted and more).	CIS option	CIS option
CIS Database Management Interface (access control and more)	CIS Option + CIP E Option	CIS Option + CIP E Option
Part search DIGIKEY, FARNELL, FUTURE, MOUSER, ARROW	CIS option	CIS option
Import		
PSpice schematic, EDIF, PDIF, XML	•	•
PADS schematic design	•	•
Import Altium schematic design	•	•
Import Eagle schematic design	•	•
Export		
PDF, DXF,EDIF, XML, ISCF	•	•
• SIMULATION PSPICE		
Simulation		
DC sweep, AC sweep, & transient analysis	•	•
SPICE Monte Carlo Analysis	•	•
SPICE Sensitivity Analysis	•	•
SPICE Worst Case Analysis	•	•
SPICE Parametric Sweep analysis	•	•
Temperature sweep analysis	•	•
Checkpoint/Restart analysis	•	•
Advanced convergence control / options	•	•

• Fonction incluse

Feature	OrCAD PSpice Designer	OrCAD PSpice Designer Plus
Analog behavioral modeling	•	•
Interactive waveform viewer & analyzer	•	•
Auto-convergence	•	•
Partial design simulation	•	•
Advanced control option	•	•
Multi-core engine support	•	•
Tcl customization for custom analysis / post-processing	•	•
Expression support for post-processing	•	•
Digital Worst Case with built-in 6 levels support	•	•
Frequency Response Analysis	•	•
PSpice Reports	•	•
		•

Models		
Stimulus editor	•	•
33,000+ simulation-ready parts	•	•
IBIS / DML model support	•	•
Model Editor for device characterization	•	•
Model development using PSpice Device Model Interface	•	•
Magnetics Part Editor	•	•
Library Encryption using AES 256 bit algorithm	•	•

PSpice Advanced Analysis (PAA)		
Advanced Analysis		•
Advanced Sensitivity: Identifies critical circuit components		•
Optimizer: Optimizes key circuit components		•
Optimizer: Curve fitting		•
Advanced Monte Carlo: Analyzes statistical circuit behavior and yield		•
Smoke: Detects component stress		•
Parametric Plotter: Solution analysis through nested sweeps		•
PSpice Device Model Interface Model Simulation		•

• Fonction incluse